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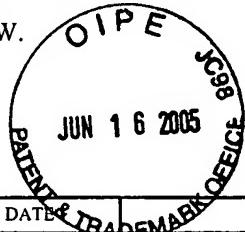
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7590 04/18/2005

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(Depositor's name)

(Signature)

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/082,055	02/26/2002	Kohtaro Gotoh	100001-00072 100021-00074	5415

TITLE OF INVENTION: TEST CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT EFFECTIVELY CARRYING OUT VERIFICATION OF CONNECTION OF NODES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/18/2005
				06/20/2005 HAL122	00000160 10082055
EXAMINER	ART UNIT	CLASS-SUBCLASS			
RAYMOND, EDWARD	2857	01 EL:1501 02 FC:1504 03 FC:8001	702-117000	1400.00 OP 300.00 OP 30.00 OP	

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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1. **Arent Fox PLLC**

2. _____

3. _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

FUJITSU LIMITED

KAWASAKI, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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 Publication Fee (No small entity discount permitted)
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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27
 b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature _____

Date **June 16, 2005**

Typed or printed name _____

Sam Huang

Registration No. **48,430**

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